D+	D-	State
0	0	Se0
0	1	Low speed detect
1	0	Full speed detect
1	1	Bus error

FIG.1

FIG.2
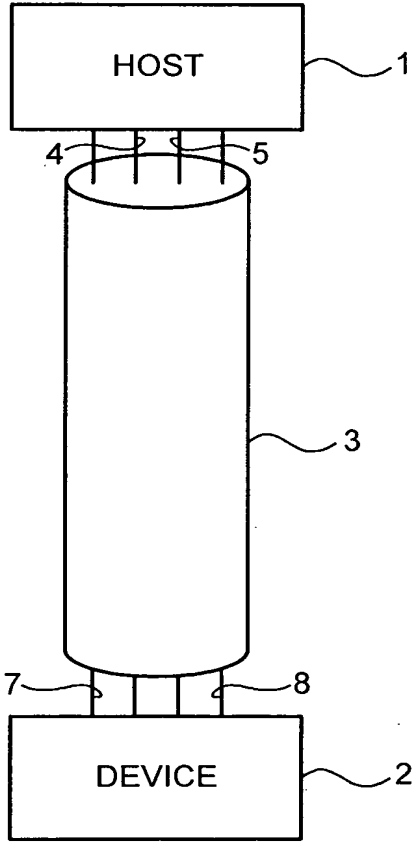
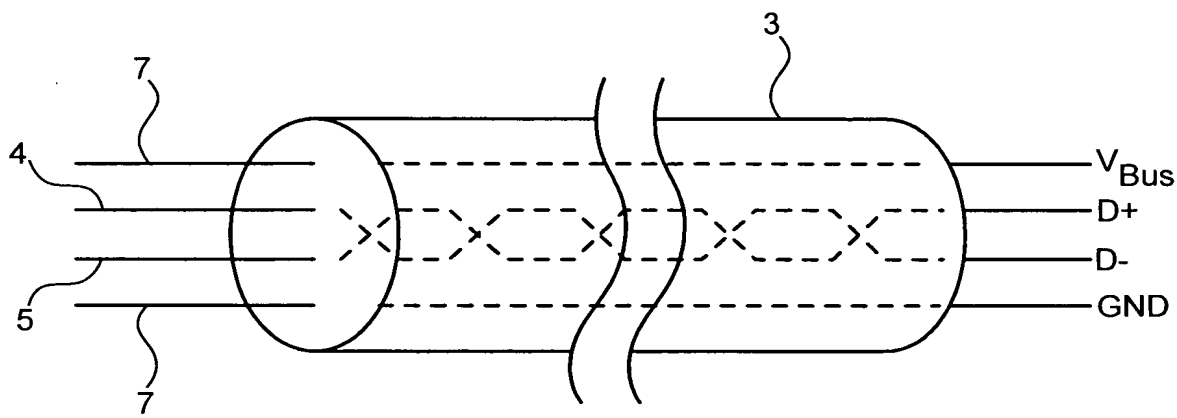**FIG.3**
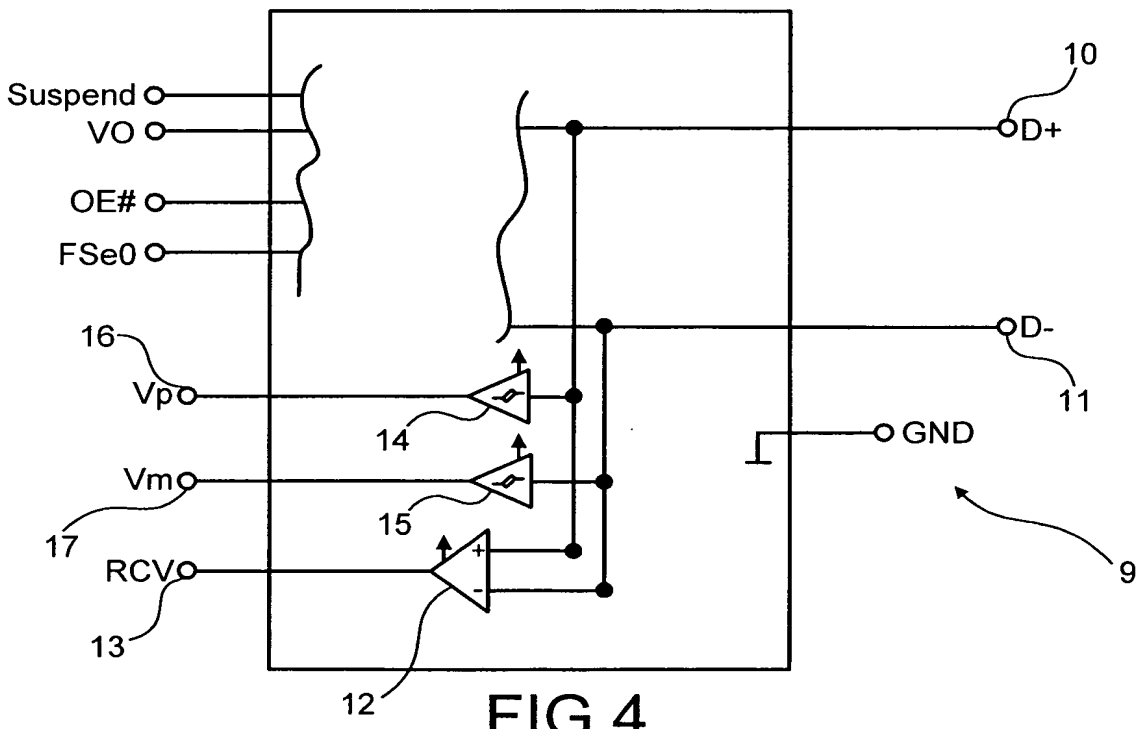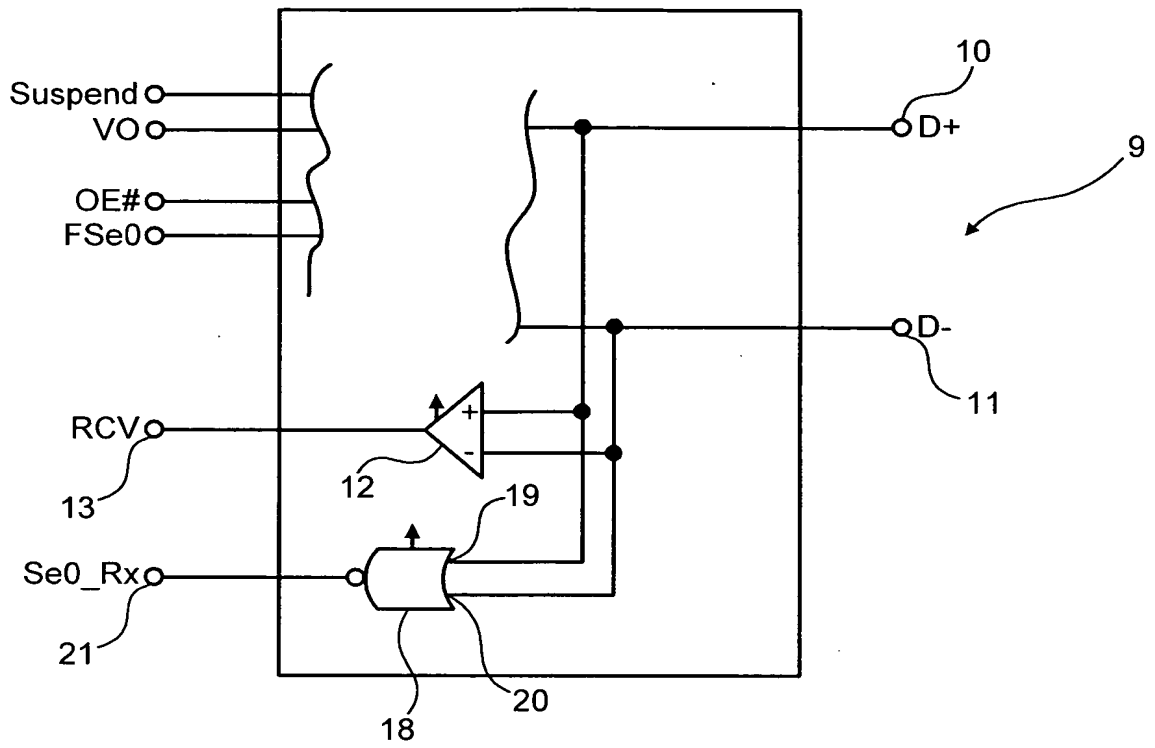

FIG.4

PRIOR ART

Suspend○

VO○

OE#○

FSe0○

10

○D+

9

○D-

11

RCV○

12

13

Se0_Rx○

21

19

20

18

FIG.5

TO HOST

3

23   24   25   9

22

FIG.6